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** CONTINUING DATA *****

This application is a CON of 09/903,580 07/13/2001 PAT 6,632,744

** FOREIGN APPLICATIONS *****

JAPAN 2000-215093 07/14/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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| Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials _____ | STATE OR COUNTRY JAPAN | SHEETS DRAWING 52 | TOTAL CLAIMS 3 | INDEPENDENT CLAIMS 1 |
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TITLE

Manufacturing method of semiconductor integrated circuit device

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